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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/086,878	03/01/2002	Andreas Dicks	PHDE 010057	1980

7590 04/07/2005  
U.S. Philips Corporation  
580 White Plains Road  
Tarrytown, NY 10591

EXAMINER

ISMAL, SHAWKI SAIF

ART UNIT	PAPER NUMBER
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2155

DATE MAILED: 04/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

RECEIVED  
APR 26 2005  
OIP/ELJ/115

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/086,878	DICKS, ANDREAS	
	<b>Examiner</b>	<b>Art Unit</b>	
	Shawki S Ismail	2155	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on March 01, 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-6 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-6 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

1. Claims 1-6 are presented for examination.

Applicant's claim for foreign priority is acknowledged.

References in applicant's IDS form 1449 have been considered.

### **Specification**

#### **Content of Specification:**

- a. Title of the Invention: See 37 CFR 1.72(a) and MPEP § 606. The title of the invention should be placed at the top of the first page of the specification unless the title is provided in an application data sheet. The title of the invention should be brief but technically accurate and descriptive, preferably from two to seven words may not contain more than 500 characters.
- b. Background of the Invention: See MPEP § 608.01(c). The specification should set forth the Background of the Invention in two parts:
  - (1) Field of the Invention: A statement of the field of art to which the invention pertains. This statement may include a paraphrasing of the applicable U.S. patent classification definitions of the subject matter of the claimed invention. This item may also be titled "Technical Field."
  - (2) Description of the Related Art including information disclosed under 37 CFR 1.97 and 37 CFR 1.98: A description of the related art known to the applicant and including, if applicable, references to specific related art and problems involved in the prior art which are solved by the applicant's invention. This item may also be titled "Background Art."
- c. Brief Summary of the Invention: See MPEP § 608.01(d). A brief summary or general statement of the invention as set forth in 37 CFR 1.73. The summary is separate and distinct from the abstract and is directed toward the invention rather than the disclosure as a whole. The summary may point out the advantages of the invention or how it solves problems previously existent in the prior art (and preferably indicated in the

Background of the Invention). In chemical cases it should point out in general terms the utility of the invention. If possible, the nature and gist of the invention or the inventive concept should be set forth. Objects of the invention should be treated briefly and only to the extent that they contribute to an understanding of the invention.

- d. Brief Description of the Several Views of the Drawing(s): See MPEP § 608.01(f). A reference to and brief description of the drawing(s) as set forth in 37 CFR 1.74.
- e. Detailed Description of the Invention: See MPEP § 608.01(g). A description of the preferred embodiment(s) of the invention as required in 37 CFR 1.71. The description should be as short and specific as is necessary to describe the invention adequately and accurately. Where elements or groups of elements, compounds, and processes, which are conventional and generally widely known in the field of the invention described and their exact nature or type is not necessary for an understanding and use of the invention by a person skilled in the art, they should not be described in detail. However, where particularly complicated subject matter is involved or where the elements, compounds, or processes may not be commonly or widely known in the field, the specification should refer to another patent or readily available publication which adequately describes the subject matter.

### **Claim Rejections - 35 USC §102**

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

3. Claims 1-7, 12-24, and 29-34, are rejected under 35 U.S.C. 102(e) as being anticipated by **Wyland** U.S. Patent No. **6,701,398**.

Art Unit: 2155

4. As to claim 1, Wyland teaches an integrated circuit comprising a processor (1), a data memory (2) and function blocks (4,5), in which a data bus (3) is provided for communication between the processor (1), the data memory (2) and the functional blocks (4,5), a logical direct connection on the data bus for a transfer of data between at least two function blocks (4, 5) being provided via a handshake method, with one function block (4) acting as master and one function block (5) acting as a slave, the latter having a linear address space to which the function block (4) acting as master can have access by way of the handshake method and which address space in the function block (5) acting as a slave is mapped onto a buffer memory (Fig. 1, 2; col. 3, lines 13-31; col. 7 line 65 – col. 8, line 8).

5. As to claim 2, Wyland teaches an integrated circuit as claimed in claim 1, characterized in that, the data bus (6), on which the handshake method takes place, is a standard bus system that is standardized for on-chip bus systems (Fig. 1, 2; col. 3, lines 13-31).

6. As to claim 3, Wyland teaches an integrated circuit as per claim 1, characterized in that, the buffer memory is a ping-pong buffer (11), onto which the address space is cyclically mapped (col. 6, lines 33-44).

7. As to claim 4, Wyland teaches an integrated circuit as claimed in claim 1, characterized in that, in the handshake method the function block (5) acting as a slave, sends an acknowledge message (13) to the function block (4) acting as master, after each permitted and finalized access of the function block acting as master to the linear address ranges (col. 7, line 65 - col. 8 line 8).

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8. As to claim 5, Wyland teaches an integrated circuit as claimed in claim 1, characterized in that, in the handshake method the function block (5) acting as a slave sends an abort-try again acknowledge message (15) to the function block (4) acting as master after each rejected access of the function block (4) acting as master to the linear address ranges (col. 6, lines 33-44).

9. As to claim 6, Wyland teaches an integrated circuit as claimed in claim 1, characterized in that, in the function block (4) acting as master is a JPEG-CODEC and the function block (5) acting as a slave is a memory interface which access an externally provided storage medium (col. 4, lines 6-26).

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shawki S Ismail whose telephone number is 571-272-3985. The examiner can normally be reached on M-F 8:30 - 5:00.

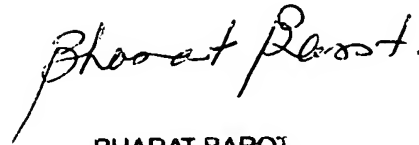
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hosain Alam can be reached on 571-272-3978. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should

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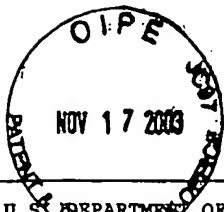
you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Shawki Ismail  
Patent Examiner  
March 31, 2005



**BHARAT BAROT  
PRIMARY EXAMINER**



Sheet 1 of 1

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE		Atty. Docket No. DE010057		Serial No. 10/087,878											
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				DAVID R. OHM											
				Filing Date MARCH 1, 2002	Group 2874										
U.S. PATENT DOCUMENTS															
Ex. Int		Document Number	Date	Name	Class	Sub- class	Filing Date If Approp.								
AA	5	5	8	1	7	4	1	12/1996	CLARK ET AL	395	500				
AB															
AC															
AD															
AE															
AF															
FOREIGN PATENT DOCUMENTS															
		Document Number	Date	Country	Class	Sub- class	Trans.								
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AH															
AI															
AJ															
AK															
OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)															
AL		Kirschbaum et al., "Rapid Prototyping of Communication Architectures", Rapid System Prototyping, 1997; Shortening The Path From Specification to Prototype Proceedings, 8 <sup>th</sup> IEEE Int'l Workshop on Chapel Hill, NC, USA, June 1997, pp. 135-141, XP01024412													
AM		G. Budd et al., "ARM7100- A High Integration, Low-Power Microcontroller for PDA Applications", Digest of Papers of COMPCON (Computer Society Conference) 1996 Technologies for The Information Superhighway, Santa Clara, Feb. 25-28, 1996, Digest of Papers of Computer Society Conference COMPCON, Los Alamitos, IEEE Comp. Soc. Press, pp. 182-187, XP010160892													
AN															
Examiner <i>Shawh. C. D.</i>										Date Considered 3-30-05					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with M 609; Draw line through citation if not in conformance and not considere Include a copy of this form with next communication to applicant.															

<b>Notice of References Cited</b>	Application/Control No. 10/086,878	Applicant(s)/Patent Under Reexamination DICKS, ANDREAS	
	Examiner Shawki S Ismail	Art Unit 2155	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,701,398	03-2004	Wyland, David C.	710/107
	B	US-6,105,119	08-2000	Kerr et al.	711/219
	C	US-6,032,178	02-2000	Bacigalupo et al.	709/208
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.